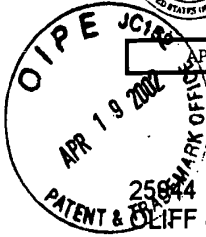




## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov



APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/052,255	01/23/2002	Akihiko Ebina	111778

25844  
CLIFF & BERRIDGE, PLC  
P.O. BOX 19928  
ALEXANDRIA, VA 22320

CONFIRMATION NO. 1930

## FORMALITIES LETTER



\*OC000000007504446\*

Date Mailed: 02/20/2002

## NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

*Filing Date Granted*

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is missing.  
*A properly signed oath or declaration in compliance with 37 CFR 1.63, identifying the application by the above Application Number and Filing Date, is required.*
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(l) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.
- The balance due by applicant is \$ 130.

---

*A copy of this notice **MUST** be returned with the reply.*

Selam  
Customer Service Center  
Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

04/22/2002 AOSHAN1 00000099 10052255

01 FC:105

130.00 OP



**PATENT APPLICATION**

Rec'd #6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of

Akihiko EBINA et al.

Attn: Box Missing Parts

Application No.: 10/052,255

Filed: January 23, 2002

Docket No.: 111778

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR  
MEMORY DEVICES

**RESPONSE TO NOTICE TO FILE MISSING PARTS WITH DECLARATION**

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

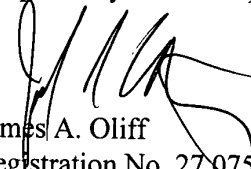
In response to the Notice to File Missing Parts of Application - Filing Date  
Granted (copy attached) mailed on February 20, 2002, submitted herewith is the executed  
Declaration of the inventors. Any specification attached to and referenced in the Declaration  
is a copy of the specification and any amendments thereto which were filed in the Office in  
order to obtain a filing date for the application.

Attached is our Check No. 129957 for ☒\$130.00 ☐\$65.00 (entitlement to  
small entity status is asserted) for the fee under 37 C.F.R. §1.16(e).

Entry of these documents should complete all of the filing formalities and fully  
satisfy all requirements of the Notice to File Missing Parts. Accordingly, examination and  
allowance of this application in due course are respectfully solicited.

The Director is hereby authorized to charge any additional fee (or credit any  
overpayment) associated with this communication to Deposit Account No. 15-0461. Two  
duplicate copies of this paper are attached.

Respectfully submitted,

  
James A. Oliff  
Registration No. 27,075

Joel S. Armstrong  
Registration No. 36,430

JAO:JSA/mlb

Date: April 19, 2002

**OLIFF & BERRIDGE, PLC**  
P.O. Box 19928  
Alexandria, Virginia 22320  
Telephone: (703) 836-6400

DEPOSIT ACCOUNT USE  
AUTHORIZATION  
Please grant any extension  
necessary for entry;  
Charge any fee due to our  
Deposit Account No. 15-0461



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Akihiko EBINA et al.

Application No.: 10/052,255

Docket No.: 111778

Filed: January 23, 2002

For: METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE INCLUDING NONVOLATILE SEMICONDUCTOR MEMORY  
DEVICES

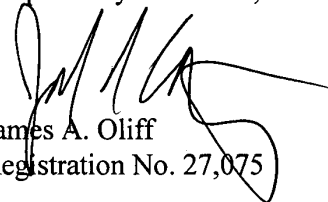
**REQUEST FOR CORRECTION OF PALM RECORDS**

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. Also attached is a corrected application data sheet. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,



James A. Oliff  
Registration No. 27,075

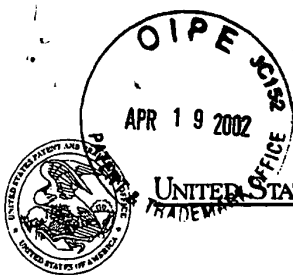
Joel S. Armstrong  
Registration No. 36,430

JAO:JSA/mlb

Date: April 19, 2002

**OLIFF & BERRIDGE, PLC**  
**P.O. Box 19928**  
**Alexandria, Virginia 22320**  
**Telephone: (703) 836-6400**

<p><b>DEPOSIT ACCOUNT USE AUTHORIZATION</b> Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>
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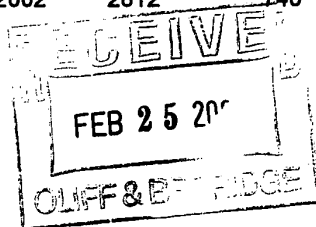


## UNITED STATES PATENT AND TRADEMARK OFFICE

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UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/052,255	01/23/2002	2812	740	111778	11	15	1

25944  
OLIFF & BERRIDGE, PLC  
P.O. BOX 19928  
ALEXANDRIA, VA 22320



CONFIRMATION NO. 1930

## FILING RECEIPT



\*OC00000007504445\*

Date Mailed: 02/20/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Akihiko Ebina, ~~Suwa-shi~~, JAPAN; FUJIMI-CHO  
Yutaka Maruo, ~~Suwa-shi~~, JAPAN; CHINO-SHI

## Assignment For Published Patent Application

SEIKO EPSON CORPORATION, Tokyo, JAPAN;  
HALO LSI DESIGN & DEVICE TECHNOLOGY, INC., Wappingers Falls, NY;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2001-21931 01/30/2001

If Required, Foreign Filing License Granted 02/19/2002

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Method of manufacturing semiconductor integrated circuit device including nonvolatile  
semiconductor memory devices